

Figure 1 shows the pin connections for the 42 decoder. The decoder is a 42-pin device with pins 1 through 15 on the left and pins 16 through 31 on the right. The connections are as follows:

- Pin 12: ZH
- Pin 13: SFWK
- Pin 14: ABUS
- Pin 15: ABUS
- Pin 16: 9
- Pin 17: 8
- Pin 18: 7
- Pin 19: 6
- Pin 20: 5
- Pin 21: 4
- Pin 22: 3
- Pin 23: 2
- Pin 24: 1
- Pin 25: 0
- Pin 26: 31
- Pin 27: 30
- Pin 28: 29
- Pin 29: 28
- Pin 30: 27
- Pin 31: 26


The decoder is labeled "42 DECODER" and "E7".

[illegible]

4H — 11 CK 574 8 — 256H

Diagram showing a 7414 hex inverter (N7 02) with inputs HBLANK and 256H, and output ALPHABLANK.

Pin diagram of the LS374 hex buffer. The chip has 16 pins. Pins 1, 2, and 3 are inputs for the first buffer (A), with pins 1 and 2 labeled PF4 and PF3 respectively. Pins 4 and 5 are outputs for the first buffer, labeled PF2 and PF1 respectively. Pin 6 is the input for the second buffer (B), labeled PF0. Pins 7 and 8 are outputs for the second buffer, labeled PF2 and PF1 respectively. Pin 9 is the input for the third buffer (C), labeled PF0. Pins 10 and 11 are outputs for the third buffer, labeled PF2 and PF1 respectively. Pin 12 is the input for the fourth buffer (D), labeled PF0. Pins 13 and 14 are outputs for the fourth buffer, labeled PF2 and PF1 respectively. Pin 15 is the input for the fifth buffer (E), labeled PF0. Pins 16 and 17 are outputs for the fifth buffer, labeled PF2 and PF1 respectively. Pin 18 is the input for the sixth buffer (F), labeled PF0. Pins 19 and 20 are outputs for the sixth buffer, labeled PF2 and PF1 respectively. The chip is labeled LS374 and has a bubble symbol indicating active-low inputs. The output pins are labeled with 'F/F' and 'D/D'.

[illegible][illegible]

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